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RYLR948

868/915MHz LoRa® Proprietary Transceiver

with Nuvoton® Arm® Cortex® M4 Module

Datasheet







PRODUCT DESCRIPTION

The RYLR948 is transceiver module feature the LoRa[®] long range modem that also provides ultra-long range spread spectrum communication and high interference immunity whilst minimizing current consumption.

Its open module architecture empowers customers to program their own firmware, granting them more flexibility and control over their end product's functionality. This versatility positions the module as a competitive choice for developers seeking a high level of customization.

FEATURES

- NUVOTON M467SJHAN & Semtech LLCC68 LoRa® Engine.
- Excellent blocking immunity
- High sensitivity

APPLICATIONS

- IoT Applications
- Mobile Equipment
- Home Security
- Industrial Monitoring and Control Equipment

BLOCK DIAGRAM



SPECIFICATION

Item	Min.	Typical	Max.	Unit	Condition
VDD Power supply	2.3	3.3	3.6	V	VDD
LoRa					
RF Output power range	0		22	dBm	
Filter insertion loss	1	2	3	dB	
RF Sensitivity	-129			dBm	
RF Input level			10	dBm	
Frequency range	820	868/915	960	MHz	
Frequency accuracy		±10		ppm	
Transmit Mode current		140		mA	RFOP = +22dBm
Receive Mode current		17.5		mA	@VDD=+3.3V
Sleep mode current		10		uA	AT+MODE=1
					@VDD=+3.3V
NUVOTON [®] M467SJHAN					
Flash		1024		KB	
SRAM		512		KB	
Weight		2.8		g	
Operating temperature	-40	25	+85	°C	

PIN DESCRIPTION

	U6		
$ \begin{array}{c} 1\\ 2\\ 3\\ 4\\ 5\\ 6\\ 7\\ 8\\ 9\\ 10\\ 11\\ 12\\ 13\\ 14\\ 15\\ 16\\ 17\\ 18\\ 19\\ 20\\ 21\\ 22\\ 23\\ 24\\ 25\\ 26\\ 27\\ 28\\ 29\\ \end{array} $	GND PB.6 PB.5 PB.4 PB.3 PB.2 PB.1 PB.0 PF.6 PC.7 PC.6 PA.7 PA.6 PA.7 PA.6 PA.7 PA.6 PA.4 PA.3 PA.2 PA.1 PA.0 PF.0 PF.1 PC.5 PC.4 NC NC VDD VDD VDD VDD GND	GND PB.7 PB.8 PB.9 PB.10 PB.11 VREF PB.12 PB.13 PB.14 PB.15 PC.14 HSUSB_VRES HSUSB_VDD33 HS USB_VDD33 HS USB_VBUS HS USB_D- HS USB_D- HS USB_D- HS USB_D- HS USB_D- HS USB_D- HS USB_ID NC NC NC NC NC NC NC NC NC NC NC NC NC	58 57 56 55 54 53 52 51 50 49 48 47 46 45 44 43 42 41 40 39 38 37 36 35 34 32 31 30

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REFLOW SOLDERING

Consider the "IPC-7530 Guidelines for temperature profiling for mass soldering (reflow and wave) processes, published 2001. Only single reflow soldering processes are recommended for REYAX modules. Repeated reflow soldering processes and soldering the module upside down are not recommended.

Preheat phase

Initial heating of component leads and balls. Residual humidity will be dried out. Please note that this preheat phase will not replace prior baking procedures.

- Temperature rise rate: max. 3 °C/s If the temperature rise is too rapid in the preheat phase it may cause excessive slumping.
- Time: 60 120 s If the preheat is insufficient, rather large solder balls tend to be generated. Conversely, if performed excessively, fine balls and large balls will be generated in clusters.
- End Temperature: 150 200 °C If the temperature is too low, non-melting tends to be caused in areas containing large heat capacity.

Heating/ Reflow phase

The temperature rises above the liquidus temperature of 217°C. Avoid a sudden rise in temperature as the slump of the paste could become worse.

- Limit time above 217 °C liquidus temperature: 40 60 s
- Peak reflow temperature: 245 °C

Cooling phase

A controlled cooling avoids negative metallurgical effects (solder becomes more brittle) of the solder and possible mechanical tensions in the products. Controlled cooling helps to achieve bright solder fillets with a good shape and low contact angle.

 Temperature fall rate: max 4 °C/s To avoid falling off, the REYAX module should be placed on the topside of the motherboard during soldering.



Recommended soldering profile

RFYAX

DIMENSIONS





unit : mm

LAYOUT FOOTPRINT RECOMMENDATIONS



unit : mm



E-mail: sales@reyax.com Website : http://reyax.com